

TIMING CONTROLLER AND CONTROLLED DELAY CIRCUIT FOR CONTROLLING TIMING OR DELAY TIME OF A SIGNAL BY CHANGING PHASE THEREOF

Abstract

A controlled delay circuit has a first gate chain, and a second gate chain. The first gate chain is used to measure a time difference between a changeover point of a first control signal and a changeover point of a second control signal. The second gate chain, which receives third signals generated in the first gate chain and representing the time difference, is used to provide an appropriate delay time from an input to an output depending on the time difference. The controlled delay circuit is capable of properly controlling the timing of the control signal according to the period of the control signal.